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Date: February 22, 2006

By: Maureen Golob
Maureen Golob



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: ROSTOKER, ET AL.

APPLICATION No.: 09/642,282

FILED: APRIL 18, 2000

FOR: **PLUG-AND-PLAY DATA CABLE WITH PROTOCOL
TRANSLATION**

PATENT No. 6,978,319 B1

ISSUED: DECEMBER 20, 2005

EXAMINER: DINH, D.

ART UNIT: 2152

CONF. No: 8938

Request for Certificate of Correction
under 37 C.F.R. § 1.322 or § 1.323

Certificate of Correction Branch
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Certificate
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of Correction

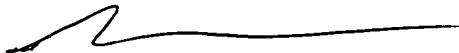
Sir:

1. The applicants request a Certificate of Correction to correct the errors in the above-identified patent, which are listed on the enclosed Form PTO/SB/44.
2. Any errors on the part of the applicant are of a clerical or typographical nature or are otherwise minor in character. None of the requested corrections would constitute new matter or require reexamination of the patent.
3. Source of Error(s) and Payment of Fee:
 - ☒ All of the errors listed on Form PTO/SB/44 are believed to be due to mistake on the part of the USPTO (37 C.F.R. § 1.322). Accordingly, no fees are believed to be due.
 - ☐ At least one of the errors occurred due to applicants' mistake made in good faith (37 C.F.R. § 1.323).
 - ☐ A check covering the fee under 37 C.F.R. § 1.20(a) (\$100.00) is enclosed herewith.

- ☐ Please charge the fee under 37 C.F.R. § 1.20(a) to Deposit Account No. 50-2207. This paper is provided in triplicate.
- ☒ Please charge any underpayment necessary for consideration of this paper to Deposit Account No. 50-2207.
4. Please send the Certificate of Correction to the undersigned at the address shown below.

Respectfully submitted,
Perkins Coie LLP

Date: February 22, 2006



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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,978,319 B1
 DATED: December 20, 2005
 INVENTOR(S): Michael D. Rostoker
 Joel Silverman

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims section, please correct as follows:

--1. A method for plug-and-play compatibility between a first apparatus capable of digitally communicating with a first protocol through a first apparatus connector and a second apparatus capable of digitally communicating with a second protocol through a second apparatus connector, said method comprising:

determining a first communication protocol received at a first apparatus connector;

enabling a first protocol driver appropriate to said determined first protocol in a first protocol processor;

translating digital communications provided at said first apparatus connector in said first protocol into digital communications at said second apparatus connector in said second protocol through said first protocol processor and then a second protocol processor; and

translating digital communications provided at said second apparatus connector in said second protocol into digital communications at said first apparatus connector in said first protocol through said second protocol processor and then said first protocol processor.--

--6. A single chip protocol translator as recited in claim 4, wherein at least one of said first driver and said second driver is loaded into said memory from at least one of said first external devices and said second external devices.--

MAILING ADDRESS OF SENDER:

PATENT NO. 6,978,319 B1

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This correction of information is required by 37 CFR 1.322, 1.323 and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collections is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.